Amendment to the specification

Substitute on page 1 of the specification, instead of first paragraph :

This application is a continuation in part of U.S. Patent and Trademark Office application S/N 10,052,082 titled "Reconfigurable Processor Architectures".

Replace with:

This application is a continuation in part of U.S. Patent and Trademark Office application S/N 10,052,082 titled "Reconfigurable Processor with Alternately Interconnected Arithmetic and Memory Nodes of Crossbar Switched Cluster ", now U.S. Patent 6,920,545.

Marked up version of replacement paragraph:

This application is a continuation in part of U.S. Patent and Trademark Office application S/N 10,052,082 titled ~~"Reconfigurable Processor Architectures"~~, "Reconfigurable Processor with Alternately Interconnected Arithmetic and Memory Nodes of Crossbar Switched Cluster ", now U.S. Patent 6,920,545.

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Substitute on page 4 of the specification, under the heading Detailed Description, instead of amended paragraph 5:

A second plurality of said interconnected functional units, also part of said stream computer, are allocated for comparing said data stream, as well as other streams internal to the computer, with a debug stream to generate debug signals. Internal streams within (internal to) said computer are other digital streams, not data stream(s) nor debug stream(s). Reporting logic is responsive to the debug signals for reporting the occurrence of matches between said data stream and said debug stream in a format compatible for human perception.

Replace with:

A second plurality of said interconnected functional units, also part of said stream computer, are allocated for comparing said data stream, as well as other streams internal to the computer, with a debug stream to generate debug signals. Internal streams within (internal to) said computer are other digital streams, not data stream(s) nor debug stream(s). An example of internal streams are those detailing the content of the element configuration register 320 shown in fig 6 of the parent application, now issued U.S. Patent 6,920,545. Reporting logic is responsive to the debug signals for reporting the occurrence of matches between said data stream and said debug stream in a format compatible for human perception.

Marked up version of replacement paragraph:

A second plurality of said interconnected functional units, also part of said stream computer, are allocated for comparing said data stream, as well as other streams internal to the computer, with a debug stream to generate debug signals. Internal streams within (internal to) said computer are other digital streams, not data stream(s) nor debug stream(s). An example of internal streams are those detailing the content of the element configuration register 320 shown in fig 6 of the parent application, now issued U.S. Patent 6,920,545. Reporting logic is responsive to the debug signals for reporting the occurrence of matches between said data stream and said debug stream

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in a format compatible for human perception.

Substitute on page 4 of the specification, instead of last paragraph:

Stream computers are collections of functional units (nodes) operationally intercon-
nected via software streams in addition to hardware links. More specifically, stream
computers comprise a plurality of similar functional units where the specification of
the computer (and the programming thereof) is defined by the operations performed
by the functional units and the streams (also known as data flows) that connect,
or traverse the functional units. Thus, the specification of a computer program for
stream computers is defined by the operations performed by the functional units and
the streams (also known as data flows) that connect the functional units.

Replace with:

Stream computers are collections of functional units (nodes) operationally intercon-
nected via software streams in addition to hardware links. More specifically, stream
computers comprise a plurality of similar functional units where the specification of
the computer (and the programming thereof) is defined by the operations performed
by the functional units and the streams (also known as data flows) that connect,
or traverse the functional units. Thus, the specification of a computer program for
stream computers is defined by the operations performed by the functional units and
the streams (also known as data flows) that connect the functional units. As further
detailed herein, figures 1 to 9, and the parent application, the plurality of functional
units (nodes) operate concurrently, each functional unit computing independently
from its neighbors.

Marked up paragraph:

Stream computers are collections of functional units (nodes) operationally intercon-
nected via software streams in addition to hardware links. More specifically, stream
computers comprise a plurality of similar functional units where the specification of

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the computer (and the programming thereof) is defined by the operations performed by the functional units and the streams (also known as data flows) that connect, or traverse the functional units. Thus, the specification of a computer program for stream computers is defined by the operations performed by the functional units and the streams (also known as data flows) that connect the functional units. As further detailed herein, figures 1 to 9, and the parent application, the plurality of functional units (nodes) operate concurrently, each functional unit computing independently from its neighbors.